

**AMENDMENTS TO THE SPECIFICATION**

**Page 2, delete paragraph [0009], and replace it with the following new paragraph:**

**[0009]** In an exemplary embodiment of the present invention provides a cache controller system which dynamically controls a cache so that a thread binary ~~coupler~~ compiler divides a program into multiple threads and loads the program ~~at~~ as a recompiled program whenever the cache loads the program from main memory and forwards the threads to a simultaneous multithreading process for processing.